

PATENT APPLICATION

JC916 U.S. PRO  
09/664094  
09/19/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*Signature*  
475  
-24-02

In re application of

Masayuki MIZUNO

Appln. No.

Group Art Unit:

Filed: September 19, 2000

Examiner:

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §§ 1.97 and 1.98**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached Form PTO-1449 and/or listed herein and which the Examiner may deem relevant to patentability of the claims of the above-identified application.

1. Japanese Unexamined Patent Application No. 8-288463, published November 1, 1996 with English Abstract.
2. Japanese Unexamined Patent Application No. 5-343564, published December 24, 1993 with English Abstract.
3. Japanese Unexamined Patent Application No. 10-326783, published December 8, 1998 with English Abstract.

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4. Japanese Unexamined Patent Application No. 11-87512, published March 30, 1999  
with English Abstract.

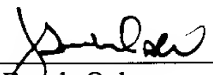
One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed (1) no later than three months from the application's filing date or (2) before the mailing date of the first Office Action on the merits (whichever is later), and therefore no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,

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